

NVIDIA TURING 11 TFLOPS, 384 TENSOR CORES, WOLF FGX

KEY FEATURES

- NVIDIA Quadro Turing TU104 GPU with 10.9 TFLOPS, 3072 CUDA Cores, 384 Tensor Cores, 48 RT Cores
- 16 GB GDDR6 256-bit memory with up to 448 GB/s
- WOLF FGX provides support for additional video formats such as SDI and CVBS
- Module power: 50-150W

ADDITIONAL FEATURES

- Four DisplayPort 1.4 digital video outputs:
 - Support for High Dynamic Range (HDR) video
 - 4K at 120Hz or 8K at 60Hz with 10-bit color depth
 - HDMI and DVI options
- Two SDI outputs, two CVBS outputs
- Turing GPGPU parallel processing:
 - 3072 CUDA® cores
 - CUDA Toolkit 10, CUDA Compute capability 7.5
 - OpenCL™ 1.2, DirectX® 12, OpenGL 4.6, OpenGL ES 3.2, Vulkan™ 1.0
- 384 Turing Cores for advanced AI inference
- 16 GB GDDR6 memory provides twice the bandwidth of the previous generation of GDDR5 memory
- HEVC (H.265) and AVC (H.264) Turing NVENC and NVDEC hardware acceleration with up to 8K encode resolution and B-frame support
- PCI Express x16
- Daisy Chain and NTB Options Available
- Windows and Linux drivers

SPECIFICATIONS

- High level of ruggedization:
 - Rugged Conduction-cooled or Air-cooled
 - Operating temperature: -40° to +85°C
 - Vibration (sine wave): 10G peak, 5 - 2000Hz
 - Shock: 30G peak for air-cooled, 40G peak for conduction-cooled
- Dimensions: 160mm x 100mm x 25.4mm
- Weight: To be Announced
- +12V or +5V Only Power Source Options
- ANSI/VITA 48, 65 (VPX-REDI, OpenVPX)



OVERVIEW

The VPX3U-RTX5000E-CV module includes both an NVIDIA® Quadro® Turing™ RTX5000 embedded GPU and a WOLF FGX in a rugged 3U VPX module. The RTX5000 includes CUDA cores for parallel processing, Tensor cores for dedicated AI inference and ray tracing cores for superior rendering speeds. The WOLF FGX provides video conversion to formats which are not native to the GPU, such as SDI and analog formats.

Getting data into and out of the module is an important consideration for this high-speed GPU. Support for GDDR6 memory provides twice the bandwidth of the previous generation's GDDR5 memory.

The Turing GPU with its Tensor cores provides this module with the underlying architecture required for an efficient AI inference engine. Intended to work in conjunction with TensorRT, CUDA and CuDNN, the Turing Tensor Core design adds INT8 and INT4 matrix operations, while continuing to support FP16 for higher precision workloads.

Unlocking the best performance requires the best cooling capability. WOLF's advanced cooling technology is designed to move heat using a low weight, high efficiency pipeline from the GPU die to the wedgelocks.



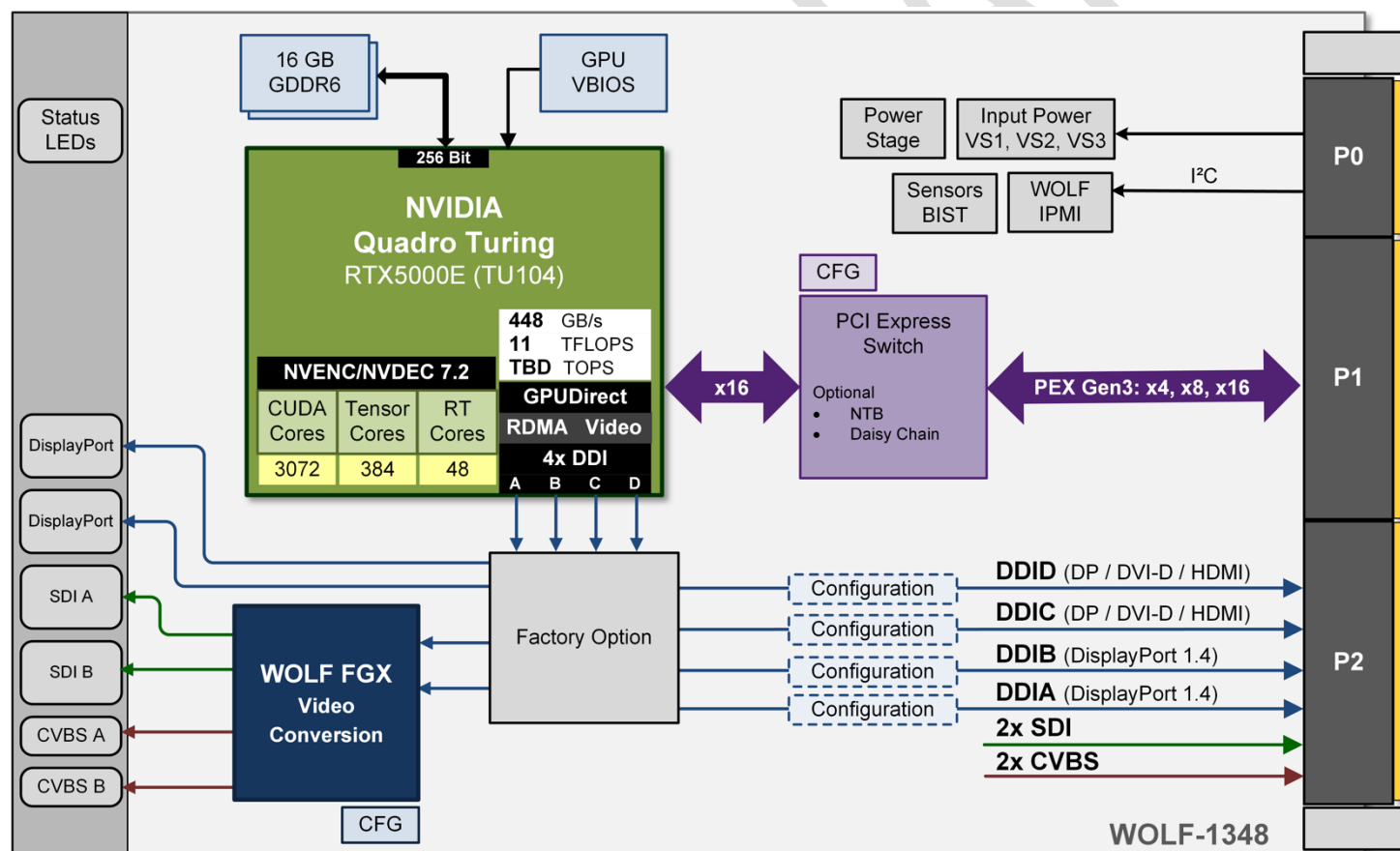
This datasheet is preliminary and is subject to change

NVIDIA TURING STREAMING MULTIPROCESSOR (SM)

The NVIDIA Turing architecture provides a 50% improvement in delivered performance per CUDA core compared to the Pascal generation. This is due to the new Turing Streaming Multiprocessor's independent integer datapath, allowing execution of concurrent integer and floating-point instructions, and the redesigned memory path which provides two times the bandwidth and more than two times the capacity for common workloads.

NVIDIA TURING TENSOR CORES

Tensor Cores are designed to speed up the tensor / matrix computations used for deep learning neural network training and inferencing operations. Turing GPUs include a new version of the Tensor Core design that has been enhanced for inferencing. Turing Tensor Cores add new INT8 and INT4 precision modes for inferencing workloads that can tolerate quantization and don't require FP16 precision.



*Front Panel Connectors on Air Cooled Versions Only

HARDWARE ACCELERATED VIDEO ENCODE / DECODE

The RTX5000E chip includes the latest generation video encode/decode hardware acceleration engine (version 7.2). This adds support for HEVC (H.265) 8K encoding at 30 fps and B-Frame support. It also provides up to 25% bitrate savings for HEVC and up to 15% bitrate savings for AVC (H.264). Using the Turing encoding engine for video encoding provides an efficient, high quality method to achieve real time 8K and 4K encoding without burdening the system CPU.

As with previous versions of the encoding engine, NVENC supports CBR and VBR rate control, programmable intra-refresh for error resiliency, and a motion estimation (ME) only mode. The NVIDIA Video Codec SDK provides a complete set of APIs, samples and documentation for hardware accelerated video encode and decode on Windows and Linux.

DESIGNED FOR SYSTEM INTEGRATION

The VPX architecture is diverse, spanning custom backplanes, an ambiguous system specification and differing input and output methodologies. That is precisely why WOLF modules come with factory configuration options to solve virtually all system integration challenges.

This WOLF module has been designed to include a WOLF FGX to support additional video formats that are not native to the Turing GPU. This also allows WOLF to react quickly to customer requirements for additional video formats.

This module has been designed to support VPX REDI (VITA 48) and OpenVPX (VITA 65).

For PCIe X8 upstream and X4 upstream / X4 downstream this module is compatible with the following OpenVPX slot profiles:

- SLT3-PAY-1D-14.2.6
- SLT3-PAY-2F-14.2.7
- SLT3-PAY-1F4U-14.2.8
- SLT3-PAY-8U-14.2.9

SYSTEM COMPATIBILITY

There are several groups spearheading initiatives to develop standards to ensure compatibility between systems and vendors, with the aim of reducing the time and cost required to deploy new technology. The module includes a new 3U VPX Interface Control Document (ICD) that includes factory configurable options which can be used to maintain compliance with many of these initiatives, including:

- SOSA (Sensor Open System Architecture)
- HOST (Hardware Open Systems Technology)
- CMOSS (C4ISR Modular Open Suite of Standards)
- E-OSA (Enterprise Open System Architecture)

PRELIMINARY INFORMATION

ORDERING CODES

The following table defines series of common order codes for the VPX3U-RTX5000E-CV module. The asterisks denote characters of the part number that are defined based on common configuration options. Some common configuration options for this module are:

- Display Interfaces
- +12V / +5V Main Power
- COTS, MCOTS or Locked
- Conformal Coating Type
- Default Power Threshold

Ordering Number	Description
3U VPX Turing RTX5000 Single Slot Configurations	
134723-****VPX3v10	3U VPX, Air Cooled, 1", NVIDIA Turing RTX5000, WOLF FGX
134733-****VPX3v10	3U VPX, Conduction Cooled, 1", NVIDIA RTX5000, WOLF FGX
134623-****VPX3v10	3U VPX, Air Cooled, 1", NVIDIA Turing RTX5000, DP++ outputs only
134633-****VPX3v10	3U VPX, Conduction Cooled, 1", NVIDIA RTX5000, DP++ outputs only

Contact Sales for the latest Ordering Numbers and available options

MANUFACTURING AND QUALITY ASSURANCE

WOLF designs modules to pass the following environmental standards:

- MIL-STD-810 (United States Military Standard for Environmental Engineering Considerations and Laboratory Tests)
- MIL-HDBK-217 (Reliability Prediction of Electronic Equipment)
- RTCA DO-160 (Environmental Conditions and Test Procedures for Airborne Equipment) on request

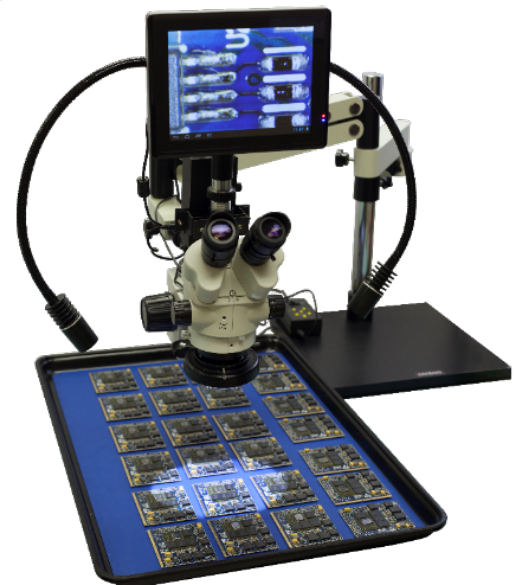
WOLF complies with the following quality management systems:

- ISO 9001:2015: Quality management systems (certified)
- SAE AS5553: Counterfeit Electronic Parts; Avoidance, Detection, Mitigation, and Disposition (compliant)
- SAE AS9100D: Quality Management System - Requirements for Aviation, Space and Defense Organizations (preparing for certification in 2019)

Boards are manufactured to meet the following standards:

- IPC-A-610 CLASS 3 (Acceptability of Electronic Assemblies)
- IPC 6012 CLASS 3 (Qualification and Performance Specification for Rigid Printed Boards, Class 3 for High Reliability Electronic Products)
- IPC J-STD-001 (Requirements for Soldered Electrical and Electronic Assemblies)

Caveat: integrated third party modules may not meet the same standards as WOLF manufactured modules.



ITAR Free

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